What Is Claimed Is:

1. A liquid crystal display device, comprising:

a plurality of gate lines on a substrate;

a plurality of data lines on the substrate orthogonal to the plurality of gate lines to define a plurality of pixel regions;

at least one thin film transistor at crossing points of the plurality of gate lines and the plurality of data lines;

a passivation film on a surface of the substrate and the at least one thin film transistor; and

a pixel electrode connected to the at least one thin film transistor,

wherein corners of the pixel electrode are arcuate shaped such that a distance between adjacent corners of adjacent pixel electrodes is larger than a distance between adjacent contact sides of the adjacent pixel electrodes.

2. The device according to claim 1, wherein the corners of the pixel electrode are rounded.

3. The device according to claim 1, wherein the pixel electrode overlaps an edge surface of at least one of the plurality of gate lines and one of the plurality of data lines.

4. The device according to claim 1, wherein the pixel electrode is transparent.

5. The device according to claim 1, wherein the pixel electrode is reflective.

6. The device according to claim 5, wherein the pixel electrode overlaps the at least one thin film transistor.

7. A liquid crystal display device, comprising:

a plurality of gate lines on a substrate;

a plurality data lines on the substrate orthogonal to the plurality of gate lines to define a plurality of pixel regions;

at least one thin film transistor at crossing points of the plurality of gate lines and the plurality of data lines;

a passivation film on a surface of the substrate and the at least one thin film transistor;

a first pixel electrode electrically connected to the at least one thin film transistor via a contact hole in the passivation film, the first pixel electrode having arcuate shaped corners such that a distance between corners of adjacent pixel electrodes is larger than adjacent contact sides of the adjacent pixel electrodes; and a second pixel electrode formed on the first pixel electrode, the second pixel electrode having arcuate shaped corners such that a distance between the corners of the adjacent pixel electrodes is larger than the adjacent contact sides of the adjacent pixel electrodes.

8. The device according to claim 7, wherein the corners of first pixel electrode and the second pixel electrode are rounded.

9. The device according to claim 7, wherein an insulating film is interposed between the first pixel electrode and the second pixel electrode.

10. The device according to claim 7, wherein the second pixel electrode includes at least one of Al, Mo, and Ti.

11. A method of forming a liquid crystal display device, comprising:

forming a plurality of gate lines on a substrate;

forming a plurality of data lines on the substrate orthogonal to the plurality of gate lines to define a plurality of pixel regions;

forming at least one thin film transistor at crossing points of the plurality of gate lines and the plurality of data lines;

forming a passivation film on a surface of the substrate and the at least one thin film transistor; and

forming a pixel electrode connected to the at least one thin film transistor,

wherein corners of the pixel electrode are arcuate shaped such that a distance between corners of adjacent pixel electrodes is larger than a distance between adjacent contact sides of the adjacent pixel electrodes.

12. The method according to claim 11, wherein the corners of the pixel electrode are rounded.

13. The method according to claim 11, wherein the pixel electrode overlaps an edge surface of at least one of the plurality of gate lines and the plurality of data lines.

14. The method according to claim 11, wherein the pixel electrode is transparent.

15. The method according to claim 11, wherein the pixel electrode is reflective.

16. The method according to claim 15, wherein the pixel electrode overlaps the at least one thin film transistor.

17. A method of forming a liquid crystal display device, comprising:

forming a plurality of gate lines on a substrate;

forming a plurality data lines on the substrate orthogonal to the plurality of gate lines to define a plurality of pixel regions;

forming at least one thin film transistor at crossing points of the plurality of gate lines and the plurality of data lines;

forming a passivation film on a surface of the substrate and the at least one thin film transistor;

forming a first pixel electrode electrically connected to the at least one thin film transistor via a contact hole in the passivation film, the first pixel electrode having arcuate shaped corners such that a distance between corners of adjacent pixel electrodes is larger than a distance between contact sides of the adjacent pixel electrodes; and

forming a second pixel electrode formed on the first pixel electrode, the second pixel electrode having arcuate shaped corners such that a distance between the corners of the adjacent pixel electrodes is larger than a distance between the contact sides of the adjacent pixel electrodes.

18. The method according to claim 17, wherein the corners of first pixel electrode and the corners of the second pixel electrode are rounded.

19. The method according to claim 17, further including forming an insulating film between the first pixel electrode and the second pixel electrode.

20. The method according to claim 17, wherein the second pixel electrode includes at least one of Al, Mo, and Ti.